

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Arkadiy MORGENSHTEIN et al

Serial No.:

10/648,474

Filed:

August 27, 2003

For:

LOGIC CIRCUIT AND METHOD

FOR LOGIC CIRCUIT DESIGN

Group Art Unit: 2819

Attorney Docket: 26327

Examiner:

Daniel D. Chang

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Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **RESPONSE TO FINAL**

Sir:

This is in response to the United States Patent and Trademark Final Office Action mailed April 4, 2006, which response is being made on August 4, 2006, and for which a one-month extension of time fee is due and enclosed herewith. A request for continued examination is also enclosed herewith. Applicant submits this response for entry into the record, in which:

> Amendments to claims begin on page 2 Remarks begin on page 14

Please amend the above-identified application as follows: